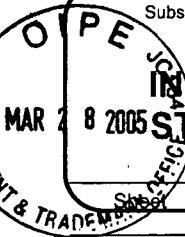


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STATEMENT BY APPLICANT**

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Sheet 1 of 4

Application Number	10/075,149
Filing Date	February 14, 2002
First Named Inventor	Milivoje Aleksic
Art Unit	2111
Examiner Name	Justin King
Attorney Docket Number	00100.02.0060

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Number-Kind Code ² (if known)					
		US- 4,920,504	4-1990	Sawada et al.	
		US- 5,384,737	1-1995	Childs et al.	
		US- 5,600,605	2-1997	Schaefer	
		US- 5,602,986	2-1997	Trevett	
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		US- 5,767,865	6-1998	Inoue et al.	
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		US- 5,854,637	12-1998	Sturges	
		US- 5,854,638	12-1998	Tung	
		US- 5,920,352	7-1999	Inoue	
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Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)						
		EP 0829820 A2	3-18-1998	Silicon Graphic		
		WO97/06523	2-20-1997	Cirrus Logic		

Examiner Signature

/Khanh Dang/ (01/22/2009)

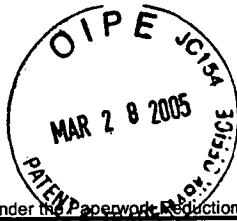
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2

of 4

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Application Number	10/075,149
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First Named Inventor	Milivoje Aleksic
Art Unit	2111
Examiner Name	Justin King
Attorney Docket Number	00100.02.0060

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		US- 6,041,417	3-2000	Hammond et al.	
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		US- 6,052,756	4-2000	Barnaby et al.	
		US- 6,076,139	6-2000	Welker et al.	
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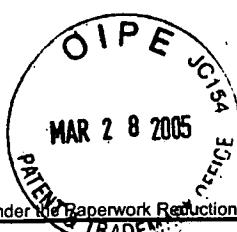
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Sheet

3

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4

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Application Number	10/075,149
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First Named Inventor	Milivoje Aleksic
Art Unit	2111
Examiner Name	Justin King

Attorney Docket Number 00100.02.0060

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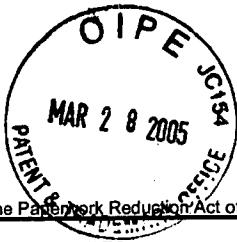
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Application Number	10/075,149
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Art Unit	2111
Examiner Name	Justin King
Sheet 4 of 4	Attorney Docket Number 00100.02.0060

Sheet

4

of

4

NON PATENT LITERATURE DOCUMENTS

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		European Search Report for Patent Application EP 0030 4528, published May 3, 2002.	
		MITSUYAMA et al.; VLSI Implementation of High Performance Burst Mode for 128 Bit Block Ciphers; IEEE 2001	

Examiner Signature	/Khanh Dang/ (01/22/2009)	Date Considered
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